

## FEE TRANSMITTAL

Electronic Version v08

Stylesheet Version v08.0

Title of Invention	Reducing Coupling Effect on Reference Voltages When Output Buffers Implemented with Low Voltage Transistors Generate High Voltage Output Signals																						
Application Number :																							
Date :																							
First Named Applicant:		Mr. Rajat CHAUHAN																					
Attorney Docket Number:		TI-38154																					
<b>TOTAL FEE AUTHORIZED \$ 856</b>																							
Patent fees are subject to annual revisions on or about October 1st of each year.																							
Filing as large entity																							
BASIC FILING FEE																							
<table border="1" style="width: 100%; border-collapse: collapse;"><thead><tr><th>Fee Description</th><th>Fee Code</th><th>Amount \$</th><th>Fee Paid \$</th></tr></thead><tbody><tr><td>Utility Filing Fee</td><td>1001</td><td>770</td><td>770</td></tr><tr><td colspan="4">Subtotal For Basic Filing Fees: \$ 770</td></tr></tbody></table>				Fee Description	Fee Code	Amount \$	Fee Paid \$	Utility Filing Fee	1001	770	770	Subtotal For Basic Filing Fees: \$ 770											
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EXTRA CLAIM FEES																							
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<b>AUTHORIZED BILLING INFORMATION</b>																							
The commissioner is hereby authorized to charge indicated fees and credit any overpayments to:																							
Deposit account number:	200668																						
Deposit name:	Texas Instruments Incorporated																						
Deposit authorized name:	Narendra R. Thappeta																						
Signature:	Naren Thappeta																						
Date (YYYYMMDD):	2004-08-10																						
Charge Assignment Fees Required Under 37 C.F.R. Section 1.21 (h).																							
Charge Any Additional Fee Required Under 37 C.F.R. Sections 1.16 and 1.17.																							